

Custom, Plug-in I/O Solution from Control Techniques and Beckhoff

Industrial Quality Easy to Use CTNet Rev D Compliant



### **CTNet Bus Coupler**

- Designed jointly by CT and Beckhoff
- Communications via CTNet Fieldbus
- Accepts most Beckhoff I/O Modules
- Requires SyPT Pro and one or more SM-Apps or SM-Apps + modules to use
- Only available from CT

#### Electronics powered by regulated +24 volt DC supply



This powers the electronics of the entire system.

The six spring-loaded fingers on the side buss the power and the local K-buss communications to all other modules.

#### Separate 24 volt supply powers the contacts



These hidden knife-edge contacts buss the 24 volts to all other I/O modules.

These are used with the digital inputs/outputs.

Powering the contacts on the CTNet Bus Terminal automatically busses contact power down the line.

Gnd +24 RED = +24 volts **BLUE** = ground **CTNet** 80 80 00  $^{\circ\circ}$ О 00 CHEALTHY O  $\circ$ О 0 00 00 OO $^{\circ\circ}$ ŌŌ 00 $\bigcirc$ 00 00 00  $\bigcirc$ 0 OO $\bigcirc$ BUS ERR 8 COURCE RUN NET ERR OO $\Omega$  $\cap \cap$  $\cap \cap$  $\cap \cap$ ററ ന്ര ገበ ററ ΥΥ OVO RUN BE( 0 1 OOOO()ጋ( ን ()()()()) П

#### The last module must be a KL9010 Beckhoff End Terminal



#### **Bus Coupler includes a 3-pin CTNet Rev D Connector**

The CTNet connector is wired as shown:



#### **DIP-switch selects any CTNet address from 1 to 64**

## The address you select must be unique.



This setting selects CTNet address 4

Node	DIP Switch			1	Node	DIP Switch					1	Node	DIP Swite			tch	I			
Address	6	5	4	3	2	1	Address	6	5	4	3	2	1	Address	6	5	4	3	2	1
1	0	0	0	0	0	0	23	0	1	0	1	1	0	45	1	0	1	1	0	0
2	0	0	0	0	0	1	24	0	1	0	1	1	1	46	1	0	1	1	0	1
3	0	0	0	0	1	0	25	0	1	1	0	0	0	47	1	0	1	1	1	0
4	0	0	0	0	1	1	26	0	1	1	0	0	1	48	1	0	1	1	1	1
5	0	0	0	1	0	0	27	0	1	1	0	1	0	49	1	1	0	0	0	0
6	0	0	0	1	0	1	28	0	1	1	0	1	1	50	1	1	0	0	0	1
7	0	0	0	1	1	0	29	0	1	1	1	0	0	51	1	1	0	0	1	0
8	0	0	0	1	1	1	30	0	1	1	1	0	1	52	1	1	0	0	1	1
9	0	0	1	0	0	0	31	0	1	1	1	1	0	53	1	1	0	1	0	0
10	0	0	1	0	0	1	32	0	1	1	1	1	1	54	1	1	0	1	0	1
11	0	0	1	0	1	0	33	1	0	0	0	0	0	55	1	1	0	1	1	0
12	0	0	1	0	1	1	34	1	0	0	0	0	1	56	1	1	0	1	1	1
13	0	0	1	1	0	0	35	1	0	0	0	1	0	57	1	1	1	0	0	0
14	0	0	1	1	0	1	36	1	0	0	0	1	1	58	1	1	1	0	0	1
15	0	0	1	1	1	0	37	1	0	0	1	0	0	59	1	1	1	0	1	0
16	0	0	1	1	1	1	38	1	0	0	1	0	1	60	1	1	1	0	1	1
17	0	1	0	0	0	0	39	1	0	0	1	1	0	61	1	1	1	1	0	0
18	0	1	0	0	0	1	40	1	0	0	1	1	1	62	1	1	1	1	0	1
19	0	1	0	0	1	0	41	1	0	1	0	0	0	63	1	1	1	1	1	0
20	0	1	0	0	1	1	42	1	0	1	0	0	1	64	1	1	1	1	1	1
21	0	1	0	1	0	0	43	1	0	1	0	1	0							
22	0	1	0	1	0	1	44	1	0	1	0	1	1							

#### **DIP-switch also selects the Baud Rate**

Baud rate must be the same at all nodes.



Data Rate (bits/sec)	DIP 7	DIP 8
5.0M	0	0
2.5M	1	0
1.25M	0	1
625K	1	1

Note that 2.50 M baud is the default and most usable setting

This setting selects 2.50 M baud

### Node 64 and 2.5 Mbaud DIP-switch setting



2.5 M baud, node 64

Data Rate (bits/sec)	DIP 7	DIP 8
5.0M	0	0
2.5M	1	0
1.25M	0	1
625K	1	1

Node		DIP Switch				1	Node		DII	P S	wi	tch	1	Node		DIF	'S	wi	vitch		
Address	6	5	4	3	2	1	Address	6	5	4	3	2	1	Address	6	5	4	3	2	1	
1	0	0	0	0	0	0	23	0	1	0	1	1	0	45	1	0	1	1	0	0	
2	0	0	0	0	0	1	24	0	1	0	1	1	1	46	1	0	1	1	0	1	
3	0	0	0	0	1	0	25	0	1	1	0	0	0	47	1	0	1	1	1	0	
4	0	0	0	0	1	1	26	0	1	1	0	0	1	48	1	0	1	1	1	1	
5	0	0	0	1	0	0	27	0	1	1	0	1	0	49	1	1	0	0	0	0	
6	0	0	0	1	0	1	28	0	1	1	0	1	1	50	1	1	0	0	0	1	
7	0	0	0	1	1	0	29	0	1	1	1	0	0	51	1	1	0	0	1	0	
8	0	0	0	1	1	1	30	0	1	1	1	0	1	52	1	1	0	0	1	1	
9	0	0	1	0	0	0	31	0	1	1	1	1	0	53	1	1	0	1	0	0	
10	0	0	1	0	0	1	32	0	1	1	1	1	1	54	1	1	0	1	0	1	
11	0	0	1	0	1	0	33	1	0	0	0	0	0	55	1	1	0	1	1	0	
12	0	0	1	0	1	1	34	1	0	0	0	0	1	56	1	1	0	1	1	1	
13	0	0	1	1	0	0	35	1	0	0	0	1	0	57	1	1	1	0	0	0	
14	0	0	1	1	0	1	36	1	0	0	0	1	1	58	1	1	1	0	0	1	
15	0	0	1	1	1	0	37	1	0	0	1	0	0	59	1	1	1	0	1	0	
16	0	0	1	1	1	1	38	1	0	0	1	0	1	60	1	1	1	0	1	1	
17	0	1	0	0	0	0	39	1	0	0	1	1	0	61	1	1	1	1	0	0	
18	0	1	0	0	0	1	40	1	0	0	1	1	1	62	1	1	1	1	0	1	
19	0	1	0	0	1	0	41	1	0	1	0	0	0	63	1	1	1	1	1	0	
20	0	1	0	0	1	1	42	1	0	1	0	0	1	64	1	1	1	1	1	1	
21	0	1	0	1	0	0	43	1	0	1	0	1	0								
22	0	1	0	1	0	1	44	1	0	1	0	1	1								

## Using the CTNet Remote I/O



SyPT Pro Tutorial Basic Functional Test SP to Beckhoff I/O Coupler

SyPT Pro Example: Set up a basic test of a SP and a Beckhoff CTNet I/O

Strategy: CTNet Remote I/O at CTNet0 node address 64 @ 2.5 Mb

SP / SM-Apps at CTNet0 node address 1 @ 2.5 Mb

KL 1114 / KL 2114 Digital In / Out fitted to Beckhoff

KL 3062 / KL 4032 Analog In / Out fitted to Beckhoff

CT-Comm Cable link from PC / SyPT Pro to SP on CT-RTU segment

Watch Window will provide the "view" to the Beckhoff diagnostic registers

A simple SyPT program will generate activity

The SyPT project editor will verify everything connected and functioning when on-line.

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Workspace Workspace TNet0_Node_1.DPL (SP Notes Initial Background	<pre>\$AUTHOR JPJ \$COMPANY CT \$TITLE SP Beckhoff Basic Test \$VERSION V1.0.0 \$DRIVE SH-Applications Notes() Initial // The initial task runs and completes before // any other tasks run // declare and initialize integer variable i% i% = 0 ) //Initial Background() // line 74 - document explicitly the end of the source</pre>	
Ready	Ln 49, Col 1 NUM IM	JS //.

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Workspace Workspace TTNet0_Node_1.DPL (SP Notes Initial Background	<pre>\$AUTHOR JPJ \$COMPANY CT \$TITLE SP Beckhoff Basic Test \$VERSION V1.0.0 \$DRIVE SM-Applications Notes() Initial() Background( top: // create activity to monitor and display if i% &lt; 10000 then i% = i% + 1 else i% = 1 endif // write the changing value to a place // where it is observable #20.01 = i% goto top: // main background loop ) //Background // line 74 - document explicitly the end of the source</pre>	
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#### Watch window: Basic test (CTNet I/O is node 64)





SyPT Pro Tutorial Basic Discrete Digital Input handling noncyclic data transfer

#### A number of discrete input modules are available.

Туре	Inputs	Description
KL1002	2	24V DC, filter 3.0 ms
KL1012	2	24V DC, filter 0.2 ms
KL1032	2	48V DC, filter 3.0 ms
KL1052	2	24V DC, filter 3.0 ms, with P/N switching
KL1104	4	24V DC, filter 3.0 ms
KL1114	4	24V DC, filter 0.2 ms
KL1124	4	5V DC, filter 0.2ms
KL1154	4	24V DC, filter 3.0 ms, with P/N switching
KL1164	4	24V DC, filter 0.2 ms, with P/N switching
KL1184	4	24V DC, filter 3.0 ms, with N switching
KL1702	2	230V AC
KL1712	2	120V AC/DC
KL1722	2	230V AC, no power contacts

Here's the Details on a 4-Input Discrete Input Module KL1104



How to hook up a KL1104 Digital Input Module



# All digital inputs are packed together in the lower 16 bits of Menu 1 parameters.

b31	b30	b29	b28	b27	b26	b25	b24	b23	b22	b21	b20	b19	b18	b17	b16
				-				-				-			
b15	b14	b13	b12	b11	b10	b9	b8	b7	b6	b5	b4	b3	b2	b1	b0
T15	T14	T13	T12	T11	T10	T9	T8	T7	T6	T5	T4	T3	T2	T1	T0

#### 255 bits of digital input can be configured with one Bus Coupler.

Reference	I/O Points	Reference	I/O Points
#1.00	T0 - T15	#1.08	T128 - T143
#1.01	T16 - T31	#1.09	T144 - T159
#1.02	T32 - T47	#1.10	T160 - T175
#1.03	T48 - T63	#1.11	T176 - T191
#1.04	T64 - T79	#1.12	T192 - T207
#1.05	T80 - T95	#1.13	T208 - T223
#1.06	T96 - T111	#1.14	T224 - T239
#1.07	T112 - T127	#1.15	T240 - T255

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Output	Ψ×
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Alias	\$DRIVE SM-Applications	
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	j Allas( // No boolean data type is provided	
	// by Drive Programming Language	
	\$define OFF 0	
	// CTNet RDNET and WRNET status codes	
	\$define COMMAND_COMPLETE 1	
	\$define NODE_BUSY 3	
	\$define INTERNAL_ERROR 5	
	\$define PRAM_DOES_NOT_EXIST -2	
	\$define PRAM_IS_KEAD_OWLY -3	
	\$define PRAM_OUT_OF_RANGE -5 \$define TO BOX_COMMS_EDEOR6	
	) //Alias	
	Initial()	
	E Background()	
Task Manager 4 ×	// line 110	
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CPU Free: 95%		
Background		
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#### Watch window: Read 4 bits of Digital Inputs (CTNet I/O is node 64)

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BKIOnode% (1.3: CTNet0_Node_1)	=64		
BKIOmenu% (1.3: CTNet0_Node_1)	=1		
BKIOparameter% (1.3: CTNet0_Node_1)	=0		Fetch the result
status% (1.3: CTNet0_Node_1)	=1		
input% (1.3: CTNet0_Node_1) =2	34	•	
bit0% (1.3: CTNet0_Node_1)	=0		
bit1% (1.3: CTNet0_Node_1)	=1		Unpack the bits
bit2% (1.3: CTNet0_Node_1)	=0		
bit3% (1.3: CTNet0_Node_1)	=0		
Immediate Window - type commands here			
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1: CT-RTU_Node_1		🗏 💫 Online 👘 🎵	



SyPT Pro Tutorial Basic Discrete Digital Output handling noncyclic data transfer

#### A number of discrete output modules are available.

Terminal	Outputs	Description
KL2012	2	24V DC, 0.5A
KL2022	2	24V DC, 2.0A
KL2032	2	48V DC, 0.5A, with polarity protection
KL2114	4	24V DC, 0.5A
KL2124	4	5V DC
KL2134	4	24V DC, 0.5A, with polarity protection
KL2184	4	24V DC, 0.5A, with N switching
KL2602	2	Relay outputs, 230V AC, 2A, normally open
KL2612	2	Relay outputs, 125V AC, 0.5A, changeover
KL2622	2	Relay outputs, 230V AC, 0.5A, isolated normally
		open
KL2631	1	Relay output, 400V AC, 3A normally open
KL2702	2	Solid state outputs, 230V AC, 0.3A

Here's the Details on a 4-output Discrete Output Module KL2114

#### KL2114, KL 2134



How to hook up a KL2114 Digital Output Module



# All digital outputs are packed together in the lower 16 bits of Menu 1 parameters.

b31	b30	b29	b28	b27	b26	b25	b24	b23	b22	b21	b20	b19	b18	b17	b16
M15	M14	M13	M12	M11	M10	M9	M8	M7	M6	M5	M4	М3	M2	M1	M0

b15	b14	b13	b12	b11	b10	b9	b8	b7	b6	b5	b4	b3	b2	b1	b0
T15	T14	T13	T12	T11	T10	Т9	Т8	T7	T6	T5	T4	Т3	T2	T1	Т0

255 bits of digital output can be configured with one Bus Coupler.

Reference	I/O Points	Reference	I/O Points
#2.00	T0 - T15	#2.08	T128 - T143
#4.01	T16 - T31	#2.08	T144 - T159
#2.02	T32 - T47	#2.09	T160 - T175
#2.03	T48 - T63	#2.10	T176 - T191
#2.04	T64 - T79	#2.11	T192 - T207
#2.05	T80 - T95	#2.12	T208 - T223
#2.06	T96 - T111	#2.13	T224 - T239
#2.07	T112 - T127	#2.14	T240 - T255

#### **Digital Outputs have "MASK" bits**

b31	b30	b29	b28	b27	b26	b25	b24	b23	b22	b21	b20	b19	b18	b17	b16
M15	M14	M13	M12	M11	M10	M9	M8	M7	M6	M5	M4	М3	M2	M1	M0
b15	b14	b13	b12	b11	b10	b9	b8	b7	b6	b5	b4	b3	b2	b1	b0
T15	T14	T13	T12	T11	T10	Т9	T8	Τ7	Т6	Т5	T4	Т3	T2	T1	Т0

You have to set the MASK bit to alter a digital bit.

Example: <u>set bit 1</u> (note that 0x<number> signifies hexadecimal format)

 $#64.02.00 = 0 \times 20000$ 

 $#64.02.00 = 0 \times 20002$ 

// clear digital output bit 1

// set digital output bit 1

#64.02.00 = 0x00002

// has no effect (no mask bit set)

#### Example: Blinking bit "b1" of Digital Output at #02.00 (CTNet I/O is node 64)

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	-
Output C\Program Files\Control Techniques\SYPT\Projects\Beckhoff\SP_BKI0_WriteDigitalOutNoncyclic\C	TNet0 Node 1 DPI
No errors detected.	
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#### Example: Blinking bit "b1" of Digital Output at #02.00 (CTNet I/O is node 64)

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#### Example: Blinking bit "b1" of Digital Output at #02.00 (CTNet I/O is node 64)

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#### Example: Blinking bit "b1" of Digital Output at #02.00 (CTNet I/O is node 64)

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Workspace       # ×         Workspace       # ×         Notes       Alias         Initial       Background             Task Manager       # ×         CTNet0_Node_1.DPL       •         Initial       Background	<pre>AUTHOR Jim Lynch, Jim Jeffers %COMPANY Control Techniques %TITLE Write Digital Output Noncyclic %VERSION V1.0.0 %DRIVE SM-Applications Notes() Alias() Alias() Initial bTogglet = OFF OutValuet = 0 OutputMaskt = OUT_MASK statust = NODE_IDLE // following is to write to parameter #2.00 on node 64 BKIOnodet = 64 // node 64 for this example BKIOparameterst = 00 // parameter #2.00 parameter value dpost = 0 // decimal point in 0 position, x1 format timeoutt = 100 // 100 ms ) //Initial Background() // line 103</pre>	
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#### Example: Blinking bit "b1" of Digital Output at #02.00 (CTNet I/O is node 64)



#### Watch window:

#### Blinking bit "b1" of Digital Output at #02.00 (CTNet I/O is node 64)

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BKIOnode% (1.3: CTNet0_Node_1)	=64	
BKIOmenu% (1.3: CTNet0_Node_1)	=2	
BKIOparameter% (1.3: CTNet0_Node_1)	=0	
dpos% (1.3: CTNet0_Node_1)	=0	
status% (1.3: CTNet0_Node_1)	=1	
bToggle% (1.3: CTNet0_Node_1)	=1	
OutputMask & (1.3: CTNet0_Node_1) =983040	34	l
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SyPT Pro Tutorial Basic Analog Input handling noncyclic data transfer

## A number of analog input modules are available.

Terminal	Inputs	Description
KL3002	2	±10V, differential inputs
KL3012	2	0-20mA, differential inputs
KL3022	2	4-20mA, differential inputs
KL3042	2	0-20mA, power supply for transducers via power contacts
KL3052	2	4-20mA, power supply for transducers via power contacts
KL3062	2	±10V, single-ended inputs
KL3064	4	±10V, single-ended inputs

#### Here's the Details on a 2-input Analog Input Module KL3062



**12-bit A/D Converter** 



How to hook up a KL3062 Analog Input Module



## Analog Inputs are collected into Menu 3

Reference	I/O Point
#3.00	0
#3.01	1
#3.02	2
#3.xx	XX
#3.98	98
#3.99	99

#### Analog Inputs are "scaled", 32767 is always full output.

Input Range	Minimum Input	Maximum Input	Resolution
0-10V	0V = 0	+10V = 32767	5mV
±10V	-10V = -32768	+10V = 32767	5mV
0-20mA	0mA = 0	20mA = 32767	5μΑ
4-20mA	4mA = 0	20mA = 32767	<b>4</b> μA



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#### Watch window: Read the first analog input (CTNet I/O is node 64)

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SyPT Pro Tutorial Basic Analog Output handling noncyclic data transfer

## A number of analog output modules are available.

Terminal	Inputs	Description
KL4002	2	0 -10V DC
KL4012	2	0-20mA
KL4022	2	4-20mA
KL4032	2	±10V DC

#### Here's the Details on a 2-output Analog Output Module KL4032



How to hook up a KL4032 Analog Output Module



## Analog Outputs are collected into Menu 4

Reference	I/O Point
#4.00	0
#4.01	1
#4.02	2
#4.xx	XX
#4.98	98
#4.99	99

#### Analog Outputs are "scaled", 32767 is always full output.

Input Range	Minimum Input	Maximum Input	Resolution
0-10V	0V = 0	+10V = 32767	5mV
±10V	-10V = -32768	+10V = 32767	5mV
0-20mA	0mA = 0	20mA = 32767	5μΑ
4-20mA	4mA = 0	20mA = 32767	4μA



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#### Watch Window: Write to the very first Analog Output

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# CTNet / Beckhoff Remote I/O System supports Cyclic Data Transfers !



CTNet Remote I/O Why use Cyclic Data Transfers ?

- FAST
- **EFFICIENT**
- NO PROGRAMMER INTERVENTION

Note: this can only be set up with the SyPT Pro !



Set up the Uni-SP to generate the Cyclic Data synchronizing message. SM-Apps is in slot 3

#17.11 = 10	Clock task set at 10 ms
#17.22 = 0	CTNet token ring ID
#17.23 = 1	node address
#17.24 = 2	baud rate = 2.5 Mbits
#17.25 = 510	FAST cyclic rate = 10 msec SLOW cyclic rate = 50 msec

#17.00 = 1070 - reset

**SyPT Pro Example:** Let's use Cyclic Data to read a digital Input

Strategy: CTNet Remote I/O is at node address 64

Four Digital inputs are configured

Digital Inputs are at parameter #01.00 on node 64

Digital Inputs are "packed" into a "collection" by the BKIO

We will send digital inputs , via cyclic data, from node 64 parameter #01.00 to node 1, parameter #73.10 (\_S10%), as a "collection"

The SP / SM-Apps at node 1 will generate the Cyclic Data synchronizing message at 10 msec

#### Example: project SP\_BKIO\_Digital\_IN\_Minimal\_Cyclic

#### Example: Minimal Digital Input cyclic version (CTNet I/O is node 64)

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#### Example: project SP\_BKIO\_Digital\_IN\_Minimal\_Cyclic

#### Set up a cyclic link from the Beckhoff to the Unidrive.

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#### Example: project SP\_BKIO\_Digital\_IN\_Minimal\_Cyclic

#### Set up a cyclic link from the Beckhoff to the Unidrive.

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#### **Open / create the Following DPL program on Unidrive, Node 1**

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#### **Open / create the Following DPL program on Unidrive, Node 1**

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SyPT Pro Example: Let's use Cyclic Data to write a digital output

Strategy: CTNet Remote I/O is at node address 64

Four Digital outputs are configured

Digital Outputs are at parameter #02.00 on node 64

Digital Outputs are "unpacked" from a "collection" by the BKIO

We will send digital outputs , via cyclic data, from node 1 parameter #72.10 (\_R10%) to node 64 parameter #02.00, as a "collection"

The SP / SM-Apps at node 1 will generate the Cyclic Data synchronizing message at 10 msec

### Example: project SP\_BKIO\_Digital\_OUT\_Minimal\_Cyclic

#### Example: Minimal Digital Output cyclic version (CTNet I/O is node 64)



### Example: project SP\_BKIO\_Digital\_OUT\_Minimal\_Cyclic

#### Set up a cyclic link from the Unidrive to the Beckhoff.

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### Example: project SP\_BKIO\_Digital\_OUT\_Minimal\_Cyclic

#### Set up a cyclic link from the Unidrive to the Beckhoff.

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#### Watch Window:

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**SyPT Pro Example:** Let's use Cyclic Data to read a analog input

Strategy: CTNet Remote I/O is at node address 64

Two Analog inputs are available, we will read both

Analog Inputs start at parameter #03.00 on node 64

Analog Inputs consume one 32-bit word each on the BKIO

Analog Inputs are signed quantities (two's complement)

We will send two analog inputs , via cyclic data, from node 64 parameter #03.00 to #03.01, to node 1 parameters #73.10 to #73.11 (\_S10% to \_S11%), as two 32-bit integers

The SP / SM-Apps at node 1 will generate the Cyclic Data synchronizing message at 10 msec

### Example: project SP\_BKIO\_Analog\_IN\_Minimal\_Cyclic

#### Example: Minimal Analog Input cyclic version (CTNet I/O is node 64)



### Example: project SP\_BKIO\_Analog\_IN\_Minimal\_Cyclic

#### Set up a cyclic link from the Beckhoff to the Unidrive.

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### Example: project SP\_BKIO\_Analog\_IN\_Minimal\_Cyclic

#### Set up a cyclic link from the Beckhoff to the Unidrive.

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SyPT Pro Example: Let's use Cyclic Data to write an analog output

Strategy: CTNet Remote I/O is at node address 64

Two Analog outputs are available, we will write to both

Analog Outputs start at parameter #04.00 on node 64

Analog Outputs consume one 32-bit word each on the BKIO

Analog Outputs are signed quantities (two's complement)

We will send two analog outputs , via cyclic data, from node 1 parameters #74.10 to #74.11 ( \_R10% to \_R11%), to node 64 parameters #04.00 to #04.01, as two 32-bit integers

The SP / SM-Apps at node 1 will generate the Cyclic Data synchronizing message at 10 msec

### Example: project SP\_BKIO\_Analog\_Out\_Minimal\_Cyclic

#### Example: Minimal Analog Output cyclic version (CTNet I/O is node 64)



#### Example: project SP\_BKIO\_Analog\_Out\_Minimal\_Cyclic

#### Set up a cyclic link from the Unidrive to the Beckhoff.

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#### Example: project SP\_BKIO\_Analog\_Out\_Minimal\_Cyclic

#### Set up a cyclic link from the Unidrive to the Beckhoff.

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#### **Open / create the Following DPL program on Unidrive, Node 1**

📑 Initial 📑 Background Here we create activity that becomes the output values.

📳 Notes

🖹 Alias

🗄 🛜 CTNet0 Node 1.DPL (Analog Out Minimal Cyclic)

Workspace

🖃 🦳 Workspace

Here we scale the values

Here we combine the output with the mask bits and place at R10% & R11%.

The I/O automatically go out from PLC registers R10% & R11%.





#### Watch Window:

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### Hints to help get Cyclic Transfers to Work.

Build-All while offline. This also "builds" the Beckhoff I/O unit.

Go online and do a "download all". This downloads cyclic definitions into the Beckhoff I/O unit.

Remember, the Beckhoff I/O Coupler "packs" the digital I/O as sixteen points per word. Forty (40) input =  $2\frac{1}{2}$  words in and forty (40) output =  $2\frac{1}{2}$  words out.

Use the Watch window to read the Beckhoff I/O parameter #0.02. This corresponds to #17.36 on the SP/SM-Apps, and a positive number indicates that the configuration has been successfully loaded into the Beckhoff and cyclic communication is possible

Note: The Beckhoff unit can handle about 4 CTNet messages in a millisecond.

Don't set up so many Cyclic transfers that it exceeds this limit !

# **End of SyPT Tutorial**

**SyPT Pro allows the system designer to create** 

and debug a complex multi-drive application

from a single workstation.



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# FUN MU

Jim Lynch Additions by Jim Jeffers